

## A Review of Analog Concepts Required in Control

### Ideal Op Amp

$$R_{IN} = \infty \Omega$$

$$R_{OUT} = 0 \Omega$$

$$A_{V_{OL}} = GAIN_{OPENLOOP} = \infty$$

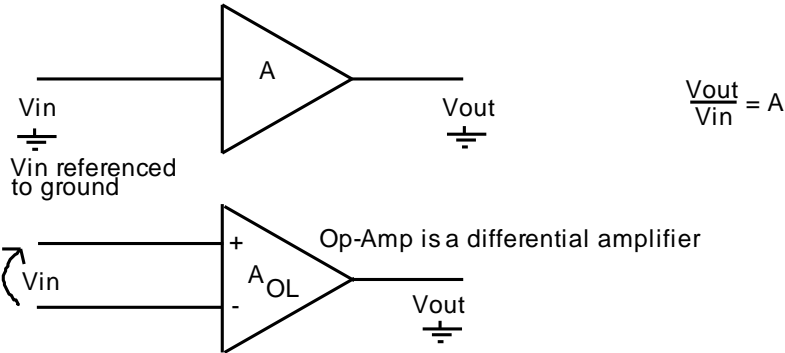
### Practical Op Amp

$$R_{IN} \approx 10 M\Omega$$

$$R_{OUT} \approx 100 \Omega$$

$$GAIN_{OPENLOOP} \approx 100,000 \text{ dB}$$

**Memorize these characteristics of op amps.**

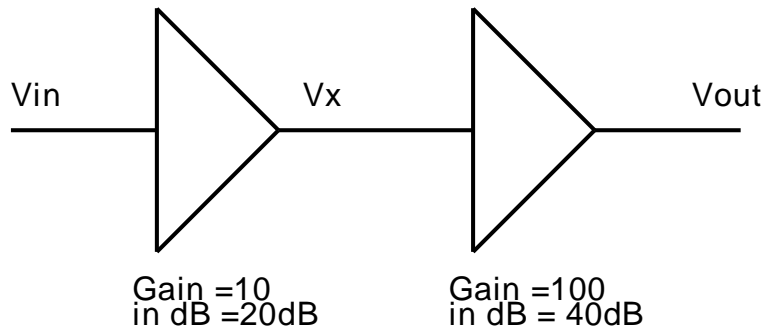


$V_{in}$  is a voltage difference between + and -,  
it is not referenced to ground

Gain in decibel of an amplifier is :  
 $20 \text{ Log}_{10} A_{Open Loop} = \text{Gain in dB}$

For amplifiers in series, the gains in dB are added to get overall gain

Example:

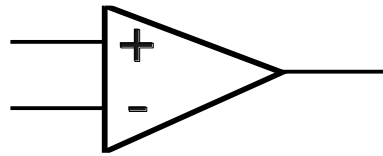


$$\frac{V_x}{V_{in}} = 10 \quad \frac{V_{out}}{V_x} = 100$$

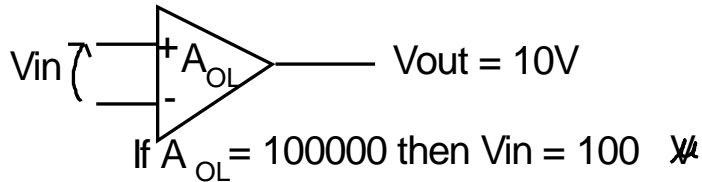
$$V_x = 10V_{in} \quad V_{out} = 100V_x = 100(10V_{in}) = 1000V_{in}$$

$$\frac{V_{out}}{V_{in}} = 1000 \text{ or } 60\text{DB}$$

### Op Amp Inputs



If the (+) input is in positive with respect to the (-) input the output of the op amp will be positive. If the (+) input is in negative with respect to the (-) input the output of the op amp will be negative.



If  $V_o = 10V$ , then the input voltage is

$$V_{in} = \frac{10}{100,000}$$

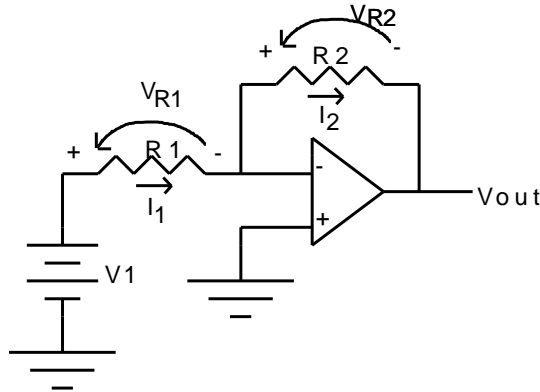
$$V_{in} = 100 \mu V$$

Since  $V_{in}$  is so small, it can be said that  $V_{IN}$  is virtually 0 Volts. The current would be very small into the op amp because of the small  $V_{IN}$  and the high  $Z_{IN}$ . Current could be as small as be 10 pA.

Inverting Amplifier

### Basic Op Amp Circuits

#### 1) The Inverting Amplifier



$$V_{R1} = V_1$$

$$I_1 = \frac{V_{R1}}{R_1} = \frac{V_1}{R_1}$$

$$I_2 = I_1$$

$$V_{R2} = I_2 R_2$$

$$V_{R2} = I_1 R_2$$

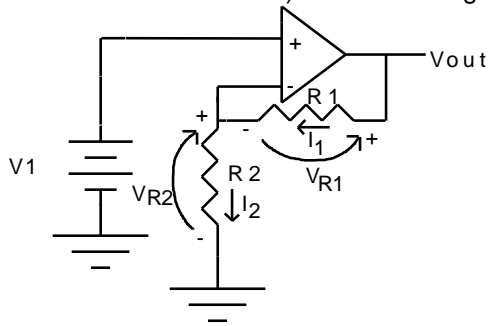
$$V_{R2} = \frac{V_1}{R_1} R_2$$

$$V_{out} = -V_{R2}$$

$$V_{out} = -\frac{V_1}{R_1} R_2$$

$$\boxed{GAIN = \frac{V_{out}}{V_1} = -\frac{R_2}{R_1}}$$

### 2) Non Inverting Amplifier



$$\begin{aligned}
 V_{R2} &= V_1 \\
 I_2 &= \frac{V_{R2}}{R_2} = \frac{V_1}{R_2} \\
 I_1 &= I_2 \\
 V_{R1} &= I_1 R_1 = I_2 R_1 = \frac{V_1}{R_2} R_1 \\
 V_{out} &= V_{R2} + V_{R1} \\
 &= V_1 + \frac{V_1}{R_2} R_1 \\
 &= V_1 \left( 1 + \frac{R_1}{R_2} \right)
 \end{aligned}$$

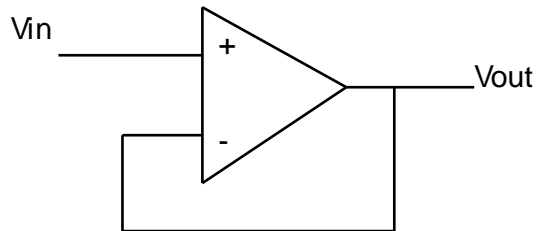
$$\text{GAIN} = \frac{V_{out}}{V_1} = \left( 1 + \frac{R_1}{R_2} \right)$$

Remember the GAIN described above is the closed loop gain.

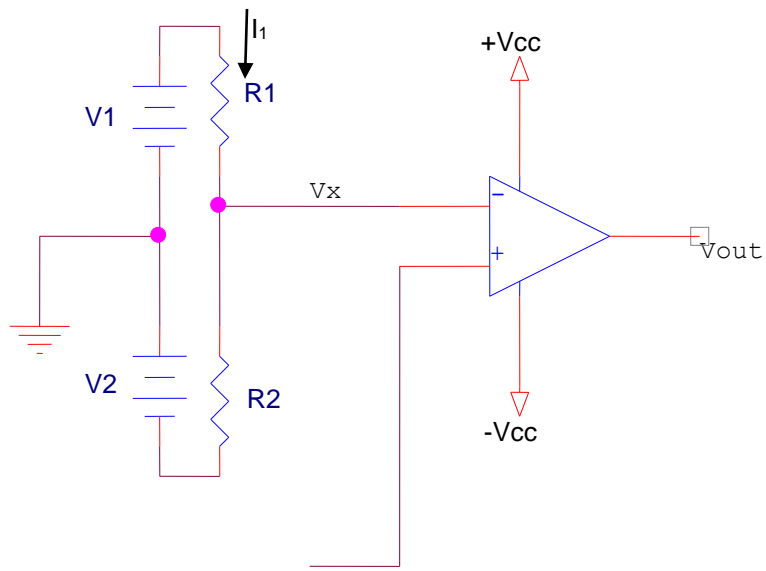
In any linear application the (+) and (-) terminals of any op amp are at zero potential.

### 3) Non-Inverting Buffer

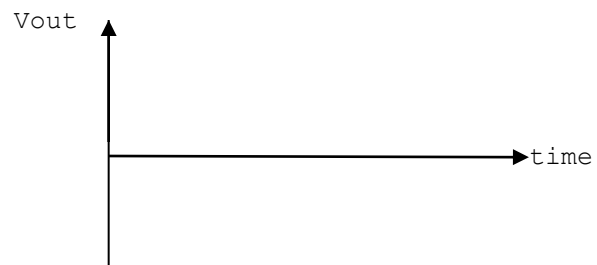
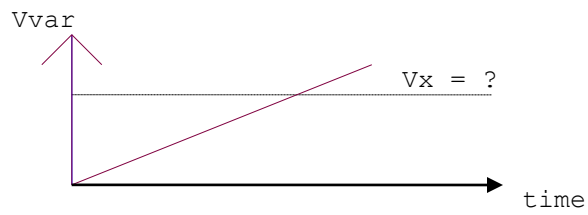
Unity Gain Buffer (Voltage Follower)

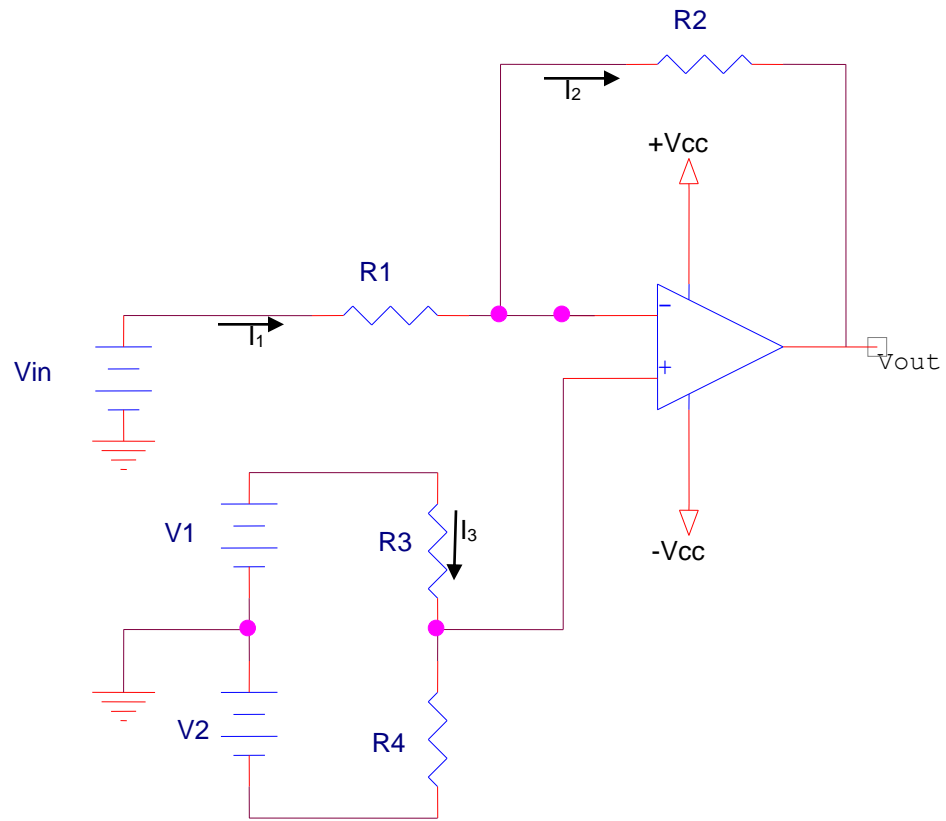


# Op Amp Circuit Analysis

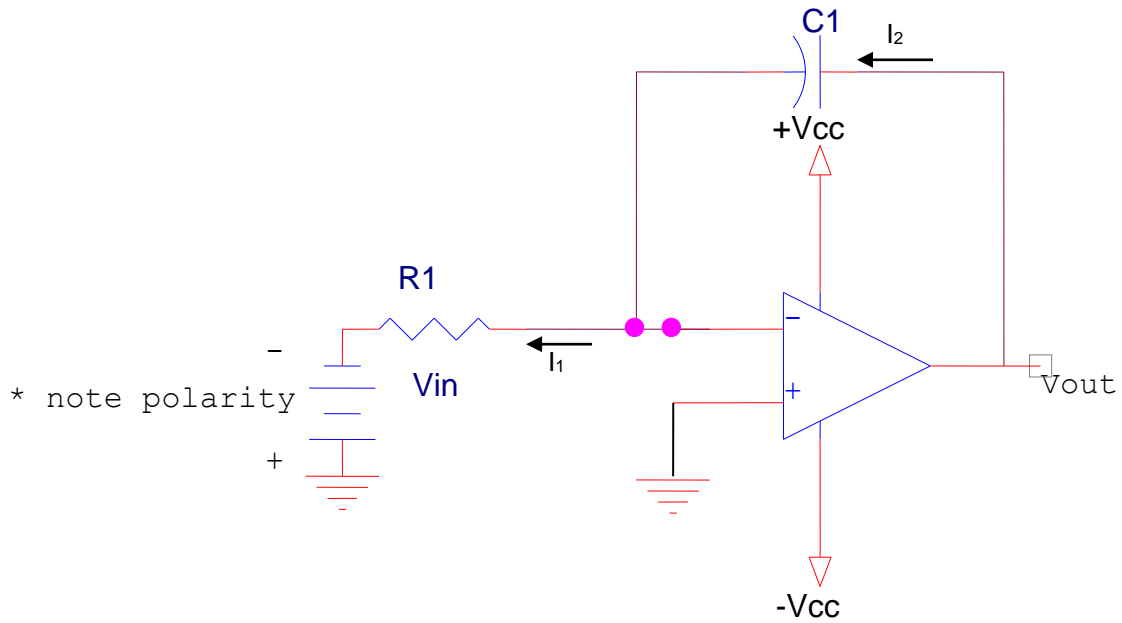


when  $V_{var}$  changes as shown below  
Sketch the output Voltage  $V_{out}$

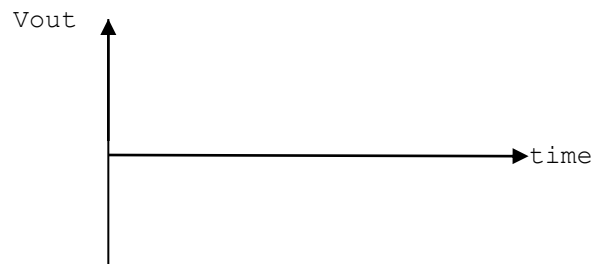


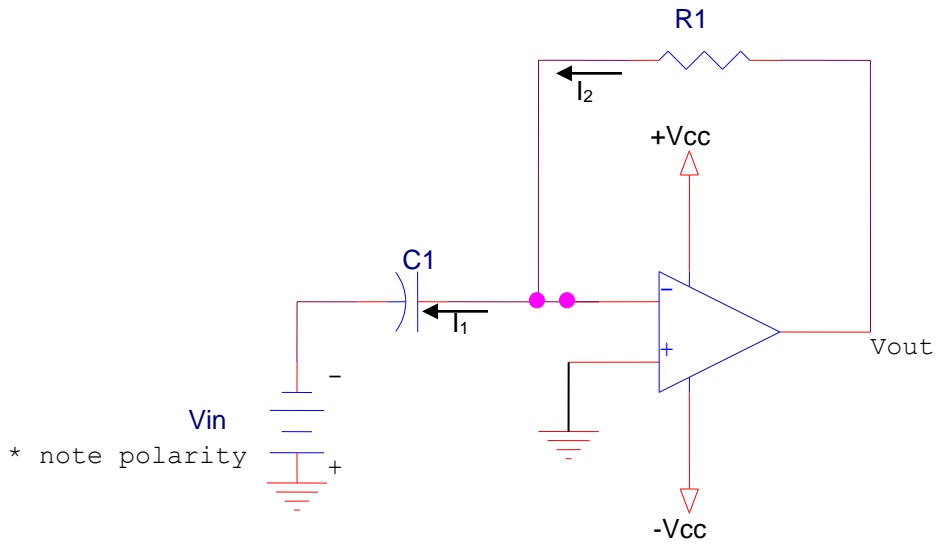


Find  $V_{out}/V_{in}$  in terms of resistors,  $V_1$ , and  $V_2$

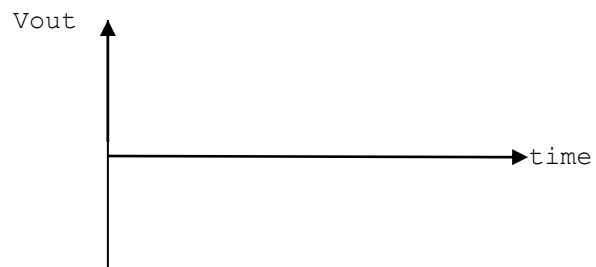


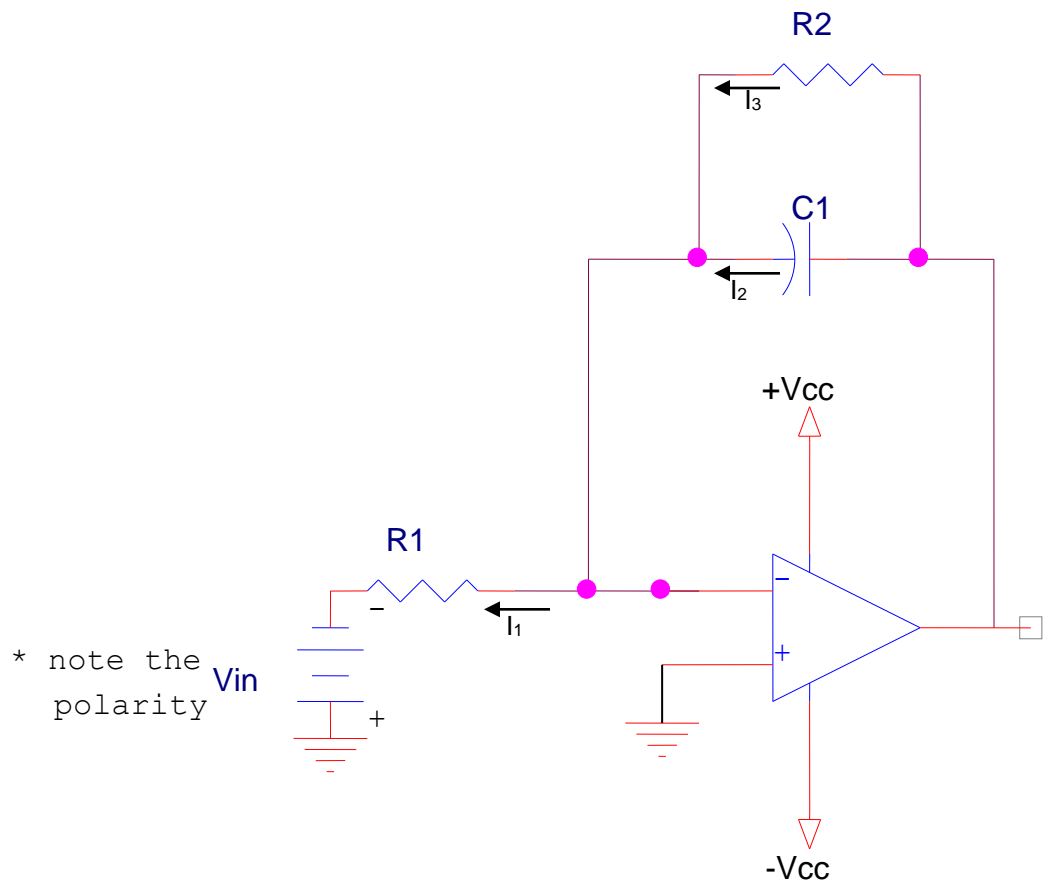
If the op amp is not saturated, describe the shape of the output voltage curve with respect to time and explain your reasons





Describe the relationship between V<sub>out</sub> and V<sub>in</sub> if the op amp is not saturated.





If the op amp is not saturated, describe the relationship between  $V_{out}$  and  $V_{in}$ .

